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Assume All Transistors Are Operating In Saturation Region And $V_{DD} = 3\text{ V}$, $V_{TH(NMOS)} = 0$... 1th, 2024EE 338L:
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Behzad Razavi, Fundamental Of Microelectronics, Wiley, 2008. Other Helpful References: Paul Gray Et Al., Analysis And
Design Of Analog Integrated Circuits, Wiley, 2009. Behzad Razavi, Design Of Analog CMOS Integrated Circuits, McGraw-Hill,
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