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Constraints You Can Specify The Maximum Area And  
Maximum Fanout Constraint. J. At This Point You Ma  
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Photonics Components Modeled In CODE V -  
SynopsysWith A  $4^\circ$  Wedge, A  $45^\circ$  Faraday Rotator, A  
Second Birefringent Crystal With A  $4^\circ$  Wedge And A  
Focusing Lens (GRIN Lens). The Crystal Axes Of The  
Two Crystals Are Oriented  $45^\circ$  To One Another. In This  
Case, The Axis Of The First Crystal Is At  $-22.5^\circ$  To The  
Wedge And The Axis Of The Second Is At  $22.5^\circ$  To The  
Wedge, Which Allows The Two ... 4th,  
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Working In Street Lighting. In This Paper, We Briefly Discuss Some Street Lighting Basics And Then Use LightTools And SLU To Design And Optimize Two Types Of Street Lamps, Including Optimization Of Their Placeme 4th, 2024.

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DesignerUsing Synopsys Design Constraints (SDC)  
With Designer 2 Timing Constraint Commands Design  
Constraint Command Examples Are Listed In Table 2.  
Clock Constraint The Create\_clock Constraint Is  
Associated With A Specific Clock In A Sequential  
Design And Determines The Maximum Register-to-  
register Delay In The Design. The Following Is A 1th,  
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Constraints (SDC ...Using Synopsys Design Constraints (SDC) With Designer 2 Timing Constraint Commands Design Constraint Command Examples Are Listed In Table 2. Clock Constraint The Create\_clock Constraint Is Associated With A Specific Clock In A Sequential Design And Determines The Maximum Register-to-register 2th, 2024.

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