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MOS Theory, SPICE, Fabrication Introduction To Digital Integrated Circuit Design Lecture 2 - 3 Recommended Reading J. Rabaey Et. Al. "Digital Integrated Circuits: A Design Perspective": Chapter 2 (2.1 - 2.3), Chapter 3 (3.3) Weste And Harris, "CMOS VLSI Design: A Circuits And Systems Perspective": Chapter 2, Chapter 3 (3.2), Chapter 5. 1th, 2024

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SPICE QDC Analysis QTransient Analysis QSubcircuits QOptimiza 3th, 2024

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Model Order Reduction Is An Attractive Technique For Reducing The Computational Cost Of Dynamical Models Simulation. In This Work, We Propose An Approach Based On Clustering, Curve- Tting, Linearization And Krylov Space Projection To Build Reduced 2th, 2024

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8: SPICE Simulation CMOS VLSI Design CMOS VLSI Design 4th Ed. 3 Introduction To SPICE Simulation Program With Integrated Circuit Emphasis – Developed In 1970's At Berkeley – Many Commercial Versions Are Available – HSPICE Is A Robust Industry Standard • Has Many Enhancements That We Will Use Written In FORTRAN 1th, 2024

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AC Analysis Of A Circuit Should Be Carried Out In The Steady State. To Prepare Ac Analysis, Run Transient Simulation Of The Circuit Until It Reaches The Steady State. In This Example, With The Duty Cycle Of 0.6, The 2th, 2024

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Adjusts All Of The Model Parameters Before Input To SPICE. For Details Of The BSIM Temperature Adjust-ment, See [6] And [7]. Temperature Appears Explicitly In The Exponential Terms Of The BJT And Diode Model Equations. In Addition, Saturation Currents Have A Built-in Temperature Dependence. The Temperature Dependence Of The 3th, 2024

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