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16M-BIT [x 1 / X 2] CMOS SERIAL FLASH 32M-BIT [x 1 / X 2 ... The MX25L1605D Are 16,777,216 Bit Serial Flash Memory, Which Is Configured As 2,097,152 X 8 Internally. When It Is In Two I/O Read Mode, The Structure Becomes 8,388,608 Bits X 2. The MX25L3205D Are 33,554,432 Bit Serial Flash Memory, Which Is Configured As 4,194,304 X 8 Internally. When It Is In Two I/O Read Mode, The Structure Be- Mar 6th, 2024Xtrinsic MMA8452Q 3-Axis, 12-bit/8-bit Digital Accelerometer12-bit/8-bit Digital Accelerometer The MMA8452Q Is A Smart, Low-power, Three-axis, Capacitive, Micromachined Accelerometer With 12 Bits Of Resolution. This Accelerometer Is Packed With Embedded Functions With Flexible User Programmable Options, Configurable To Two Interrupt Pins. Embedded Interrupt Functions Allow For Overall Power Savings Apr 11th, 20243× 16-bit Timer/Counter • 16-bit Low Energy Timer Ultra ... As A Wake-up Interrupt Controller Handling Interrupts Triggered While The CPU Is Asleep. The EFM32 Implementation Of The Cortex-M3 Is Described In Detail In EFM32G Cortex-M3 Reference Manual. 2.1.2 Debug Interface (DBG) This Device Includes Hardware Debug Support Mar 3th, 2024.

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