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Event Capture Unit
Watchdog Timer
PM ROM 2k X 24
DM RAM 1k X 16
PM RAM 2k X 24
MEMORY SERIAL PORTS
SPORT0 SPORT1
Precision Voltage Reference
Power On Reset
EXTERNAL ADDRESS BUS
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Figure 4: Block Diagram For The A
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Discrete-Time Signal Processing A Course On Digital Signal ...Discrete-Time Signal Processing, Third Edition, By Oppenheim And Schafer. Prentice Hall, 2010. ISBN-13: 978-0-13-198842-2 A Course On Digital Signal Processing, By Boaz Porat. John Wiley And Sons. (ISBN: 0-471-14961-6). Other DSP Textbooks Are Also Good References (Mitra, Proakis, Etc.)

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