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Allmän ...Condition Mainly Used With Binary Logic Elements Where The Logic State
1 (TRUE) Is Converted To A Logic State 0 (FALSE) Or Vice Versa [IEC 60617-12, IEC
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Where A Higher Physical Level Is Converted To A Lower Physical Level Or Vice Versa
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MM-9 ME-64 MM-8 ME-66 MM-7 MKE-300 MM-11 Shriber Acoustic SA-568 MM-19
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