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16-bit Pulse Counter • 16-bit Low Energy Timer LQFP48 ...The EFM32 Implementation Of The Cortex-M3 Is Described In Detail In EFM32 Cortex-M3 Reference Manual. 2.1.2 Debug Interface (DBG) This Device Includes Hardware Debug Support Mar 1th, 202416-bit Low Energy Timer • 2x 16-bit Timer/Counter Ultra ...As A Wake-up Interrupt Controller Handling Interrupts Triggered While The CPU Is Asleep. The EFM32 Implementation Of The Cortex-M3 Is Described In Detail In EFM32G Cortex-M3 Reference Manual. 2.1.2 Debug Interface (DBG) This Device Includes Hardware Debug Support Mar 19th, 2024High-Performance, 2-Ch, 24-Bit, 216kHz Sampling Multi-Bit ...High-Performance, Two-Channel, 24-Bit, 216kHz Sampling Multi-Bit Delta-Sigma Analog-to-Digital Converter •Digital High-Pass Filtering Removes DC Offset 23 4• Supports Linear PCM Output Data - Left And Right Channel Filters May Be - Output Sampling Rates From 8kHz To D May 10th, 2024.

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