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Condition Mainly Used With Binary Logic Elements Where The Logic State 1 (TRUE) Is Converted To A Logic State 0 (FALSE) Or Vice Versa [IEC 60617-12, IEC 61082-2] 3.20 Logic Inversion Condition Mainly Used With Binary Logic Elements Where A

Higher Physical Level Is Converted To A Lower Physical Level Or Vice Versa [1th, 2024

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64 3 BHM B4.E, B4.F Voltage Above Normal Or Shorted To High 64 4 BHM B4.E, B4.F Voltage Below Normal Or Shorted Low 64 5 BHM B4.E, B4.F Current Below Normal Or Open Circuit 64 6 BHM B4.E, B4.F Current Above Normal Or Shorted To Ground 65 3 BHM B4.G Voltage Above Normal Or Shorted To High 65 4 BHM B4.G Voltage Below Normal Or Shorted Low 3th, 2024

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Sandia National Laboratories, Albuquerque, NM Yuepeng Wan And Sanjay Sampath State University Of New York At Stony Brook, Stony Brook, NY To Be Published In The Proceedings Of TTSC (International Thermal Spray Conference) 2000 ASM International, Materials Park, OH, May 2000 Keywords: Air Plasma Spray, Process Map, Molybdenum, Particle Diagnostics 1th, 2024

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