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ATmega48/88/168 15.9.6 TIMSKO Timer/counter Interrupt Mask Register Bits 7..3 Res: Reserved Bits These Bits Are Reserved Bits In The Atmel ATmega48/88/168 And Will Always Read As Zero. Bit 2 OCIE0B: Timer/counter 5th, 2024ME 3200 Mechatronics I Laboratory Lab 2: LabView Computer

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