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## MICROPROCESSORS AND MICROCONTROLLERS 3 0 0 3 OBJECTIVE ...

Microprocessor/ Microcontroller Based System For A Given Control Application. ... "Microcomputer Systems: The 8086 / 8088 Family - Architecture, Programming And Design", Second Edition, Prentice Hall Of India, 2007. 3. A. K. Ray & K. M. Bhurchandi, "Advanced Microprocessors Nd A Peripherals- Architectures, Programming And Interfacing ... Mar 13th, 2024

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3. While Coming To The Lab Bring The Lab Manual

Cum Observation Book, Record Etc. 4. Take Only The Lab Manual, Calculator (if Needed) And A Pen Or Pencil To The Work Area. 5. Before Coming To The Lab, Prepare The Prelab Questions. Read Through The Lab Experiment To Familiari Jan 7th, 2024

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Working Steps For C Interrupt Programming Include Interrupt File #include Sei ( ) & Cli ( ) Instructions For I Flag. Define ISR ISR (interrupt Vector Name) { // Our Program } For Interrupt Vector Name We Must Use ISR Name From T May 13th, 2024

## MICROPROCESSORS AND MICROCONTROLLERS LAB

Demonstration Programs For 8086 Trainer Kit 21. Addition Of Two 16-bit Numbers Using 8086 Trainer Kit 22. Addition Of Two 16-bit Numbers With Respective To Address Using 8086 Trainer Kit 23. Addition Of Two Multi Byte Numbers Using 8086 Trainer Kit 24. Subtraction Of Two 16-bit Numbers Using 8086 Trainer Kit … Apr 3th, 2024

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## SET - 1 MICROPROCESSORS AND MICROCONTROLLERS

C) What Is The Use Of Bit -Set -Reset Mode Of 8255? [2M] D) What Is The Difference Between A Long Jump (LJMP) And A Short Jump (SJMP) Instruction Of 8051? [3M] E) What Is The Purpose Of Pseudo -instructions? Explain Briefly. [3M] F) Which Ports Of PIC18 Are Bit Addressable? [2M] PART -B 2. A) What Are Microprocessors? Jan 2th, 2024

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1. Yu-Cheng Liu, Glenn A.Gibson, "Microcomputer Systems: The 8086 / 8088 Family – Architecture, Programming And Design", Second Edition, Prentice Hall Of India, 2007. 2. Kenneth J Ayala, "The 8051 Microcontroller Architecture Programming And Application", 2nd Edition, Pen Apr 3th, 2024

## Microprocessors, Lecture 5: AVR Microcontrollers -Timers

University Of Tehran 37 Accessing 16-bit Registers In AVR • TCNT1=0x05ff, We Want To Save The Content Of TCNT1 In R20 And R21 • Cannot Read TCNT In One Cycle – AVR Is A 8-bit Machine • Read TCNT1L (0xff) At T0, At The Same Cycle Occurs TCNT=0x0600 • Read TCNT1H (0x06) • The Content Mar 12th, 2024

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