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Also Assume The Con-volution Kernel Size Is $K \times L$. Then The Computation In The Convolution Layer Can Be Represented In A Nested-loop Description, As Shown In Fig. 2. The Array X Contains The Input Feature Maps, And The Array Y Contains The Output Feature Maps Which Are Initial-ized to Zeros. The array W Contains the weights in the convolution kernels ... Feb 9th, 2024

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SNrram: An Efficient Sparse Neural Network Computation Architecture Based On Resistive Random-Access Memory Peiqi Wang^{1,2*}, Yuji ^{1,2}, Chi Hong¹, Yongqiang Lyu¹, Dongsheng Wang¹, And Yuan Xie² Tsinghua University¹, University Of California, Santa Barbara² Wpq14@mails.tsinghua.edu.cn¹, Wds@mail.tsinghua.edu.cn¹, Yuanxie@ece.ucsb.edu² ABSTRACT Mar 13th, 2024

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Co-Design Of Deep Neural Nets And Neural Net Accelerators For Embedded Vision Applications Kiseok Kwon,1,2 Alon Amid,1 Amir Gholami,1 Bichen Wu,1 Krste Asanovic,1 Kurt Keutzer1 1 Berkeley AI Research, University Of California, Berkeley 2 Samsung Research, Samsung Electronics, Seoul, South Korea {kiseo May 11th, 2024

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