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PDP TV Lily TRAINING MANUAL - JUCETIZETRAINING MANUAL. Agenda 1.

Specification 2. Alignment & Adjustment 3. Block Diagram 4. Wiring Diagram ...

Logic B'd Display Data Driver Timing Scan Timing Input Data Processor Clock:

27MHz Data Controller Clock: 60MHz DRAM Driver ... Plasma Panel Display

Structure PDP Filter (Corning) Module (SDI) SMPS (SEM) Display Board (SEC) 8th,

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