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Chapter 8 Memory Hierarchy And Cache Memory • Suppose Processor Has 2 Levels Of Hierarchy: Cache And Main Memory •  $T_{\text{Cache}} = 1$  Cycle,  $T_{\text{MM}} = 100$  Cycles • What Is The AMAT Of The Program From Example 1?  $AMAT = T_{\text{Cache}} + MR_{\text{Cache}} (t_{\text{MM}}) = [1 + 0.375(100)]$  Cycles = 38.5 Cycles Memory Performance Example 2 2th, 2024.

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The Memory/Storage Hierarchy And Virtual MemoryStorage Device Speed Vs. Size Facts:

- CPU Needs Sub-nanosecond Access To Data To Run Instructions At Full Speed
- Faststorage (sub-nanosecond) Is Small (100-1000 Bytes)
- Big Storage (gigabytes) Is Slow (15 Nanoseconds)
- Hugestorage (terabytes) Is Glaciallyslow (milliseconds)

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Secondary Memory Memory Hierarchy Secondary Memory Memory Hierarchy: In Modern Computers, There Are Several Types Of Memory: • Cache: RAM Technology Capacity 256K- 1 M, 10 Nanoseconds • Main Memory: RAM - ROM Technology 100 M- 1G, 100 Nanoseconds • Secondary Storage (Disk): 10 G-1000 G, 10-30 Milliseconds • Tert 2th, 2024 Cache Memory And Performance Memory Hierarchy 1 Memory Hierarchy 19 CS@VT Computer Organization II ©2005-2015 CS:APP & McQuain Caches Cache: A Smaller, Faster Storage Device That Acts As A Staging Area For A Subset Of The Data In A Larger, Slower Device. Fundamental Idea Of A Memory Hierarchy: - For Each K, The Faster, Smaller Device At Level K Serv 3th, 2024 Understanding Memory Con Gurations For In-Memory ... Understanding Memory

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