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Chapter 8 Memory Hierarchy And Cache Memory • Suppose Processor  
Has 2 Levels Of Hierarchy: Cache And Main Memory • T Cache = 1 Cycle, T MM = 100 Cycles • What Is The AMAT Of The  
Program From Example 1?  $AMAT = T_{Cache} + MR_{Cache} (t_{MM}) = [1 + 0.375(100)] \text{ Cycles} = 38.5 \text{ Cycles}$  Memory  
Performance Example 2 3th, 2024

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Updated David M. Zar ... 2th, 2024.

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