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 Gp (dB) PL (W) PEP Fig.9 Power Gain As A Function Of Load Power; Typical Values.
 Class-AB Operation; $V_{DS} = 50\text{ V}$; $IDQ = 0.7\text{ A}$; $R_{GS} = 5\Omega$; $F1 = 28.000\text{ MHz}$; $F2 =$
 28.001 MHz . Handbook, Halfpage 0 60 40 20 0 100 200 Jun 1th, 2024.
 BLF248 VHF Push-pull Power MOS Transistor 0 50 100 150 200 0 100 MGP206 R_{DSon}
 (m Ω) T_j ($^{\circ}\text{C}$) Fig.7 Input And Output Capacitance As Functions Of Drain-source
 Voltage; Typical Values Per Section. $V_{GS} = 0$; $F = 1\text{ MHz}$. Handbook, Halfpage 0 May
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 (W) T_h ($^{\circ}\text{C}$) (1) (2) Rev. 06 - 5 December 2006 3 Of 15. NXP Semiconductors Product
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 CMOS Circuits Consume Much Less Power Than Other Types Of Circuits. Figure 6–7b
 Illustrates How NFET And PFET Can Be Fabri-cated On The Same Chip. Portions Of
 The P-type Substrate Are Converted Into N-type Wells By Donor Implantation And

Diffusion. Conta Jan 2th, 2024.

Lecture 12: MOS Transistor Models
C VV CV V Qy $- + - \approx -$ Source End Drain End ()
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