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WaveRunner Xi-A Series - Teledyne LeCroy CAN, LIN, FlexRay,™ And MIL-STD-1553 Serial Triggers, Available For WaveRunner Xi-A. 10. "Push" Knobs Trigger Level, Delay, And Offset Knobs All Provide Shortcuts To Common Actions When Pushed. 11. Local Language User Interface Select From 10 Language Preferences. Add A Front Panel Overlay With Your Local Language. 5 2th, 2024 AP015 Current Probe Operator's Manual - Teledyne LeCroy ADP015 Current Probe 2 922254-00 Rev A Observe All Terminal Ratings. To Avoid Electric Shock Or Fire, Do Not Use The Probe Above The Current & Voltage Limits Shown On The Probe As Well As All Accessories. Do Not Remove Probe Casing. Removing The Probe's Case Or Touching Exposed Co 1th, 2024 Remote Control Manual - Teledyne LeCroy Oscilloscope And The Setting Of Remote Addresses. In This Chapter, The Basic Remote Control Concepts Common To Both GPIB And RS-232-C Are Introduced. Also Presented Is A Brief Description Of The Remote Control Messages. The Following Two Chapters Set Out How To Send Program Messages

Over The 1th, 2024.

Differential Amplifier Operators Manual - Teledyne  
LeCroyInput Configuration True Differential, + And -  
Inputs Precision Voltage Generator Can Be Selected As  
- Input Source In V COMP Mode. Offset Capability  
Precision Voltage Generator Can Be Used To Provide  
True Differential Offset. +Input Coupling Selections AC,  
Off (Precharge), DC -Input Coupling Selection AC, Off  
(Precharge), DC, V COMP 2th, 2024DA 1855A -  
Operator's Manual - Teledyne LeCroyInput  
Configuration True Differential, + And -Inputs Precision  
Voltage Generator Can Be Selected As - Input Source  
In V COMP Mode. Offset Capability The Precision  
Voltage Generator Can Be Used To Provide True  
Differential Offset. +Input Coupling Selections AC, Off  
(Precharge), DC -Input Coupling Selection AC, Off  
(Precharge), DC, V COMP 2th, 2024PETracer Edge User  
Manual - Teledyne LeCroyChapter 1: Overview  
PETracer Edge User Manual 4 Teledyne LeCroy 1.2  
CATC Trace Software PETracer Edge Utilizes The CATC  
Trace™ To Assi St Users In Analyzing How PCI Express  
Components Work Together, Diagnose Problems, And  
Test For ... 1th, 2024.

PXD Digitizer Series Operator's Manual - Teledyne  
LeCroy• Quick Reference Guide • CD ROM Contact The  
Nearest LeCroy Customer Service Center Or National  
Distributor If Anything Is Missing Or Damaged  
Immediately. LeCroy Is Not Responsible For  
Replacement If Not Contacted Immediately. ... The

Digitizer's Firmware Has Been Thoroughly Tested An  
3th, 2024Expanding Applications - Teledyne

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Probing Have Been Addressed Through A Wide Variety  
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Include A Slot Interposer, Custom Form-factor  
Interposers Such As M.2 And U.2 (SFF-8639), Mid-bus  
Probes, And Multi-lead Solder-down Probes, All Of  
Which Support A Wide Range Of Data Rates. 2th,

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LeCroyModel Technology. LabWindows/CVI Users  
Should Use The IVI-C Driver, Which Includes An  
Function Panel File For Selecting Driver Functions As Is  
Standard In LabWindows. Developers Working In .NET  
Environments W 2th, 2024.

EMC/ESD Test Solutions Datasheet - Teledyne

LeCroyEMC/ESD Test Solutions Key Features Fast And  
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Fall, And Width Parameters Histogram Up To 2 Billion  
Events Long Memory Allows The Capture Of Comple

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Sheet Oscilloscopes Debug With Confi Dence 100 MHz

- 350 MHz Key Specifi Cations Bandwidth 100 MHz,

200, 350 MHz Channels 2 Or 4 Memory Up To 14

Mpts/Ch (28 Mpts Interleaved) Sample Rate Up To 1

GS/s / 2 GS/s Interleaved Display 7" Bright T 3th,

2024LeCroy ProtoSync Decode Annotation And

Protocol Analyzer ...(for PCI Express), LeCroy's Voyager

Protocol Suite (for USB 3.0, 2.0 And 1.x), LeCroy's Sierra (for SAS, SATA, And Fibre Channel) Hardware Protocol Analyzer Product Family. Therefore Those Already Familiar With LeCroy Protocol Analyzers Will Find It E 3th, 2024.

Cycle To Cycle Jitter Of CPU Clicks - Teledyne

LeCroy Incorporate Multiple, Phase Locked Loop (PLL) Based Frequency Multipliers. Figure 1 Shows A Block Diagram Of A Typical Clock Distribution System. The Master Clock Is A 16 MHz Crystal Oscillator. A PLL Based Frequency Multiplier/buffer Doubles The Clock Frequency And Provides Multiple Buffered Outputs. The Resulting 32 MHz Clock Is Then 3th, 2024 LeCroy WaveMaster 8 Zi-A Series Datasheet Lating The Longest Record Lengths And Performing The Most Complex ... Transmitter, Receiver, Cable And Fixture Compliance Testing Requirements For Standards Such As: • SATA • USB • PCI Express • HDMI • SAS • Fibre Channel • DisplayPort SERIAL DATA PHYSICAL LAYER COMPLIANCE TESTING SERIAL DATA PHYSICAL LAYER COMPLIANCE TESTING ... 1th, 2024 LeCroy ProtoSync PE Datasheet Integrated 2nd Display Provides Quickest Insight LeCroy's Zi-EXTDISP-15 Is An Display That Allows The Most Interactive And Intuitive Debug Experience. Since Both The Oscilloscope And Second Display Screens Are Touch Friendly, It Is Quick And Easy To Navigate Through The Oscilloscope Decode Annotation Or The Protocol Analyzer Decode Views. 2th, 2024.

PCI Express Test QPHY-PCIe - Teledyne LeCroyElectro-mechanical Specification Rev 1.1 And 2.0. Testing Requirements For Both System Boards And Add-in Cards Are Described By The Specification. QPHY-PCIe Has The Ability To Test Compliance In Accordance With The Specification For Both System Boards And Add-in Cards. By Integrating The SIGtest Libraries Into The Oscilloscope Software, 1th, 2024ActiveDSO Developer Guide - Teledyne LeCroyDevelopers' Guide 1 About ActiveDSO ActiveDSO™ Is An ActiveX® Control That Enables Teledyne LeCroy Oscilloscopes To Be Controlled By And Exchange Data With A Variety Of Windows® Applications Or Programming Languages That Support The ActiveX Standard: MS Office Programs, Internet Explorer®, Visual Basic®, Visual C++®, Visual Java, Python, And Much More. 3th, 2024How To Operate The DDA Remotely - Teledyne LeCroyThe Action, The 6 Data Values Specify It In Detail. ... The Device As The EPSON Printer (or Compatible) And The Second Indicates The GPIB Port. While The Command ... TDIV 5000 NS TDIV 5000E-3 US String Data This Data Type Ena 1th, 2024. For PCI Express 5 - Teledyne LeCroyThe Summit T54 Protocol Analyzer Features Support For The PCIe 5.0 Technology At Speeds Of Up To 32.0 GT/s And Up To X4 Link Widths. It Also Provides Up To 64GB Of Recording Memory Which Can Be Expanded To Support Up To 128GB Of Recording Memory At Up To X8 Link Widths By Cascading A Second 2th, 2024PROTObus

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